ANNIE1-electronics-design - Feature #9090

Task # 9089 (Assigned): UC ADC cards

UC ADC user manual

06/10/2015 11:47 AM - Jonathan Eisch

Status:	Assigned	Start date:	06/10/2015
Priority:	Normal	Due date:	02/29/2016
Assignee:	Jonathan Eisch	% Done:	100%
Category:		Estimated time:	2.00 hours
Target version:		Spent time:	0.00 hour

Description

Need technical details and operating instructions for UC ADC.

Key details needed:

- Maximum acquisition length.
- Maximum number of acquisitions buffered.
- Does VME readout induce deadtime?
- Self-trigger capability?
- Look-back capability? (Must the trigger signal precede sampling, or does it continuously sample into a buffer?)

Do we have access to the firmware source code?

History

#1 - 10/09/2015 01:15 PM - Jonathan Eisch

- Tracker changed from Bug to Feature

- Status changed from New to Assigned

- Assignee set to Jonathan Eisch

#2 - 12/17/2015 05:26 PM - Jonathan Eisch

- Due date changed from 06/19/2015 to 02/29/2016

The manual will change as the firmware is changed.

#3 - 01/25/2018 07:14 AM - Matthew Wetstein

- % Done changed from 0 to 100